

Electronic Patent Application Fee Transmittal

Application Number:	10734708
Filing Date:	11-Dec-2003
Title of Invention:	METHOD FOR FABRICATING TRANSISTOR GATE STRUCTURES AND GATE DIELECTRICS THEREOF
First Named Inventor:	Mark R. Visokay
Filer:	Jacqueline J. Garner./Marianna Smith
Attorney Docket Number:	TI-35227

Filed as Large Entity

Utility Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1700